

1/9 M. Ueda E.D.N. JP920000046US1

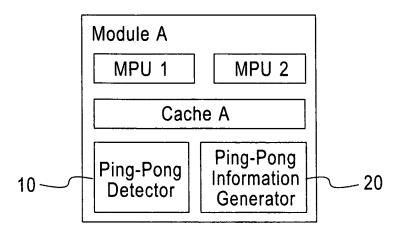


Fig. 1A

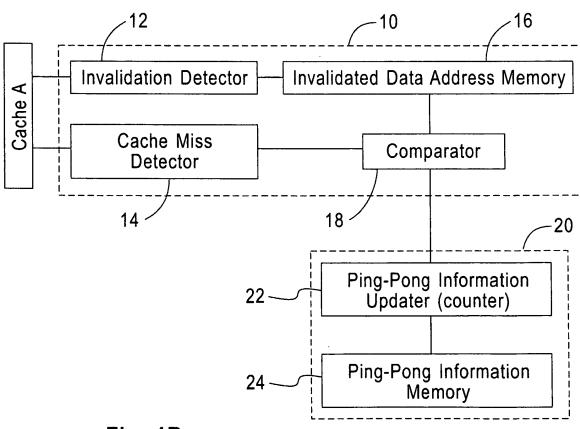


Fig. 1B

2/9 M. Ueda E.D.N. JP920000046US1

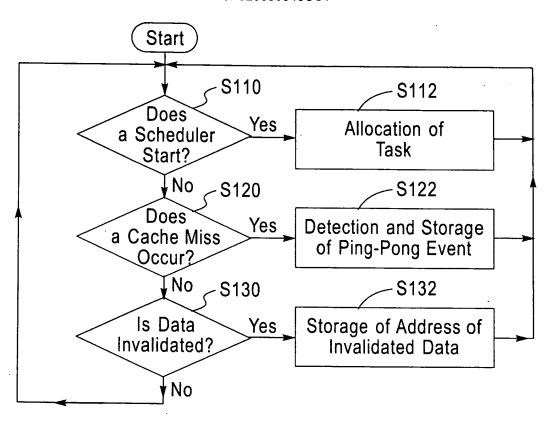


Fig. 2A

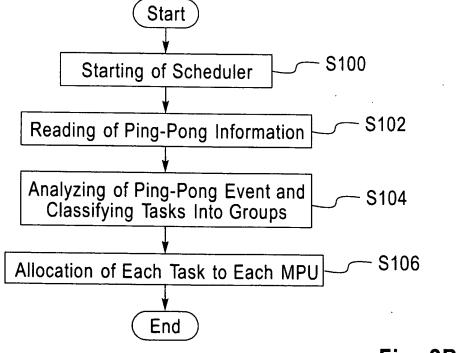


Fig. 2B

3/9 M. Ueda E.D.N. JP920000046US1

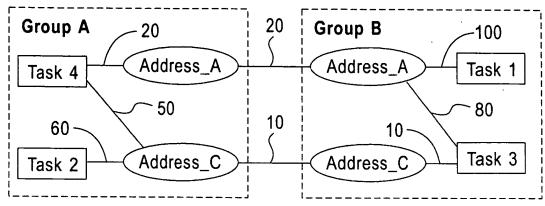


Fig. 3A

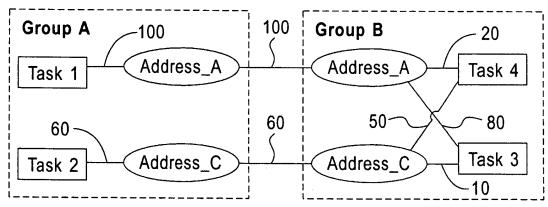
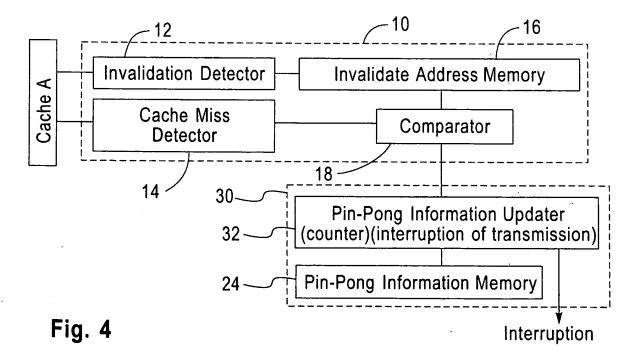


Fig. 3B



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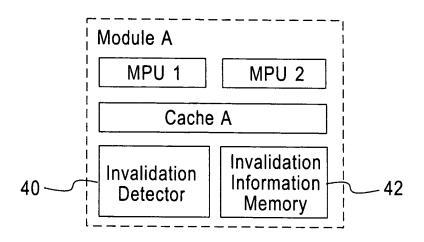


Fig. 5

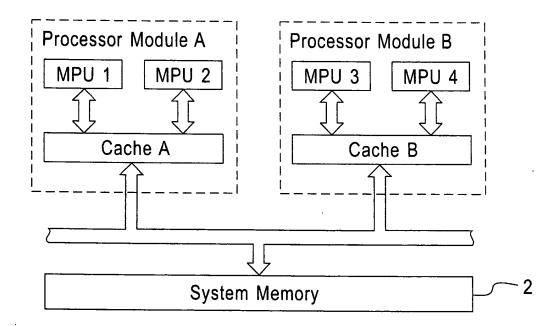


Fig. 6

## 5/9 M. Ueda E.D.N. JP920000046US1

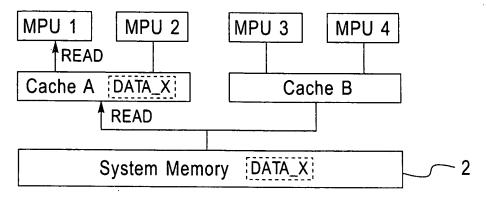


Fig. 7A

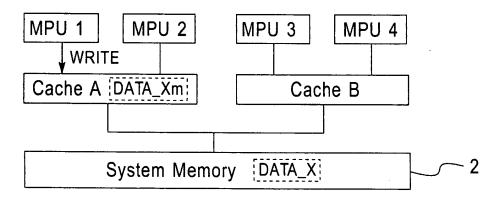


Fig. 7B

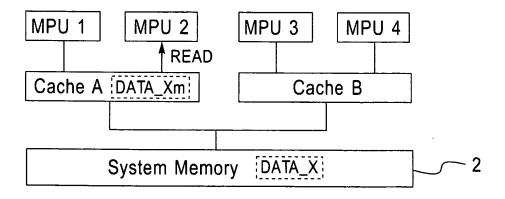


Fig. 7C

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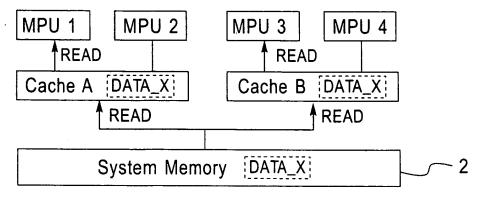


Fig. 8A

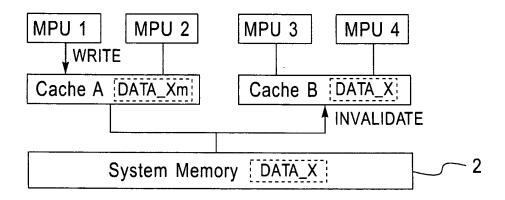


Fig. 8B

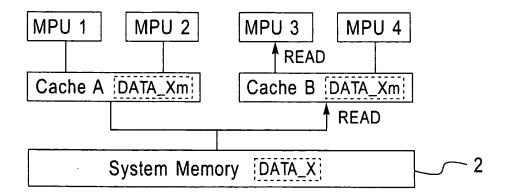


Fig. 8C

7/9 M. Ueda E.D.N. JP920000046US1

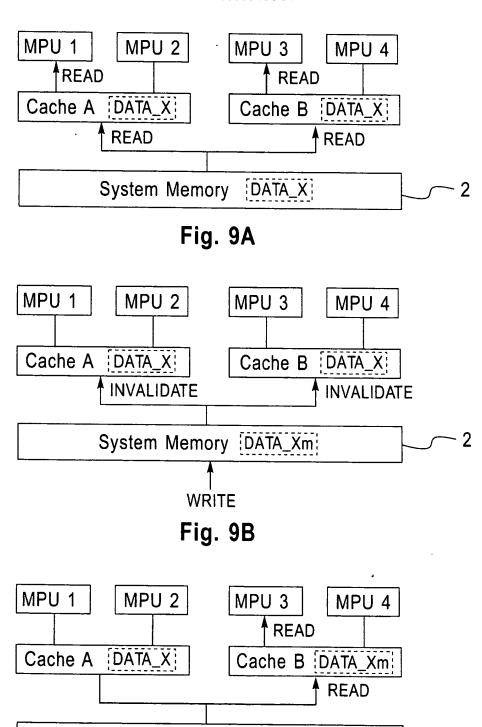
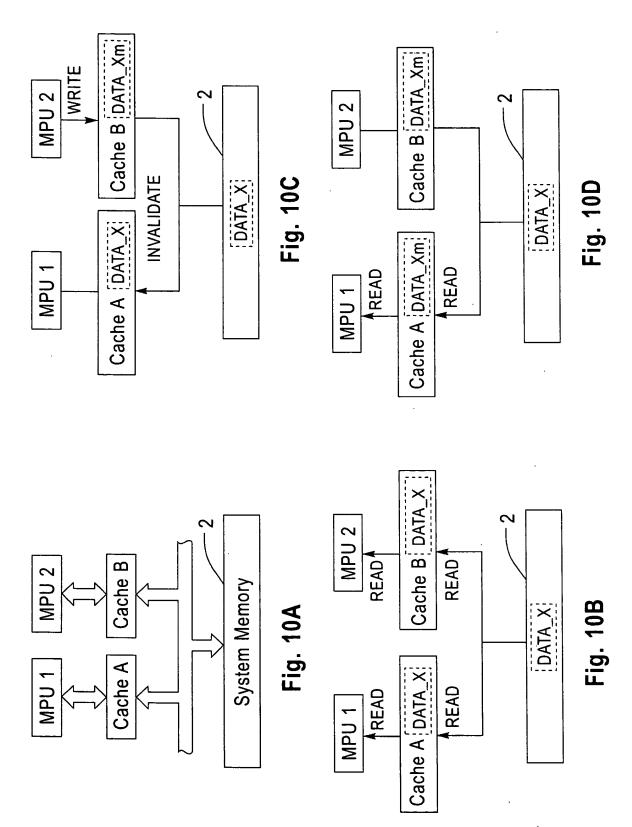


Fig. 9C

DATA\_Xm

System Memory

8/9 M. Ueda E.D.N. JP920000046US1



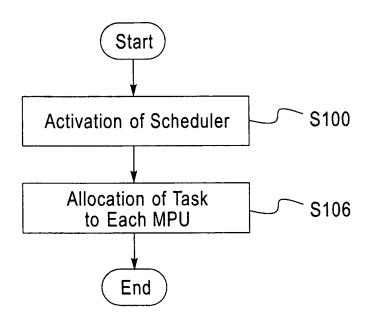


Fig. 11